Vivekananda College of Engineering & Technology,Puttur

[A Unit of Vivekananda Vidyavardhaka Sangha Puttur ®]

Affiliated to VTU, Belagavi & Approved by AICTE New Delhi

| CRM08 | Rev 1.10 | <ECE> | <14.10.2020> |

CONTINUOUS INTERNAL EVALUATION– 1

Dept: ECE	Sem / Div: **3EC A & B**	Sub: **Computer Organization and Architecture**	S Code: **18EC35**
Date: 21.10.2020	Time: 9:30-11:00 am	Max Marks: 50	Elective: N

Note: Answer any 2 full questions, choosing one full question from each part.

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Q N		Questions	Marks	RBT	COs
		PART A			
1	a	With a neat diagram, explain the basic operational concept of computer.	8	L2	CO1
	b	Evaluate (A+B) * (C+D) in three-, two-, one- and zero-address instructions.	8	L3	CO2
	c	Write a short note on – (i) Memory Locations and Addresses (ii) Big-endian and Little-endian assignment	9	L2	CO2
		OR			
2	a	Explain in brief, the key performance parameters that affect the processor performance.	8	L2	CO1
	b	Illustrate instructions and instruction sequencing with an example.	8	L3	CO2
	c	Write a short note on – (i) Represent 85.125 in IEEE floating point using single precision (ii) Condition Codes.	9	L2	CO2
		PART B			
3	a	What is addressing mode? Explain any four types of addressing modes with two examples each.	8	L2	CO2
	b	Write an assembly language program to add '*n*' numbers of an array.	8	L2	CO2
	c	How I/O devices are controlled? What is program controlled I/O? Explain with an example and sketch.	9	L2	CO2
4	a	Explain the various assembler directives used in assembly language program	8	L2	CO2
	b	Explain the shift and rotate operations with examples.	8	L2	CO2
	c	What is Stack? Explain stack operations with an examples.	8	L2	CO2

Prepared by: Suhandas   HOD